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INFORMATION DISCLOSURE STATEMENT BY APPLICANT

Page 1 of 2

Application No.	10/809,244
Filed	3/25/2004
First Inventor	Gupta, Pankaj
Art Unit	
Examiner	
Atty. Docket No.	NLMI.P162

U.S. Patent Documents

Examiner Initials*	US Patent Document		Name of Patentee or Applicant of Cited Document	Date of Publication	Relevant Pages, Columns, Lines
	Number	Kind			
	2002/0089937		Venkatachary et al.	7/1/2002	
	6,018,524		Turner et al.	1/1/2000	

Non Patent Literature Documents

Examiner Initials	Name of Author, Title of Item, Date, Page(s), Volume-Issue Number(s), Publisher, City and/or Country where Published	Translation
	A Fast and Scalable IP Lookup Scheme for High-Speed Networks, Chen et al., ©1999 IEEE, pp. 211-218.	
	Efficient hardware Architecture for Fast IP Address Lookup, Pao et al., IEEE INFOCOM 2002, PP. 555-561.	
	Fast and Scalable Layer 4 Switching, V. Srinivasan, G Varghese, S. Suri and M. Waldvogel, Presented at ACM Sigcomm '98, 16 page slide presentation	
	Forwarding Engine for Fast Routing Lookups and Updates, Yu et al., 1999 Global Telecommunications Conference, pp. 1556-1564	
	International Search Report, PCT/US2004/009531, Mailed September 2, 2004	

Examiner Signature	Date Considered
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*Examiner: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw a line through citation if not in conformance and not considered. Include a copy of this form with next communication to applicant.

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	Reconfigurable memory Architecture for Scalable IP Forwarding Engines, Akhberizedeh et al., © 2002 IEEE, pp. 432-437.	

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